

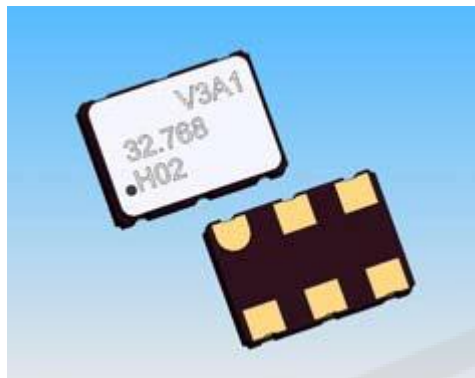


We are committed to the supply of all types of substrate wafers to the microelectronics industry

V Type VCXO

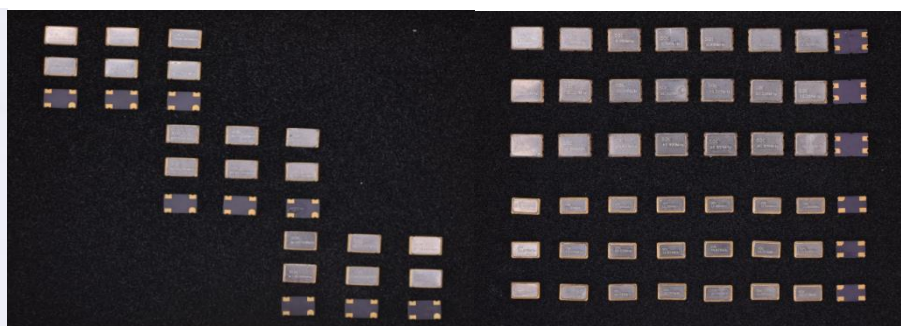
Features

- CMOS/TTL Compatible VCXO.
- Typical 7.0 x 5.0 x 1.6 mm ceramic metal SMD package.
- Resistance Seam welding for hermetical seal.
- Tape & Reel packaging for automatic assembly.
- Lead Free and RoHS compliant



ORDERING GUIDANCE

Product Type	Supply Voltage (V)	Frequency Stability (ppm)	Operating Temp. Range (°C)	OE PIN	—	APR	—	Frequency (MHz)
Code: Meaning	Code: Meaning	Code: Meaning	Code: Meaning	Code: Meaning	Dash		Dash	
V: 7X5 SMD CMOS/TTL VCXO	3: 3.3 5: 5.0	A: ±25 B: ±50 D: ±20 G: ±15 H: ±30	1: 0~+70 2: -40~+85 3: 0~+85 4: -20~+70 5: -10~+85 6: -10~+70 7: -10~+60 8: 0~+60	Default: pin2 5: pin5		Specify desired APR Value In ppm		XXX.XXXXXX
Ordering example: V3A4-100-32.768MHz 7x5 SMD CMOS/TTL VCXO, 3.3V, ±25ppm, 0°C~70°C, OE@PIN2, APR=±100ppm, 32.768MHz								
Note: If there're any other particular requirements, they should be specified in customers' order.								

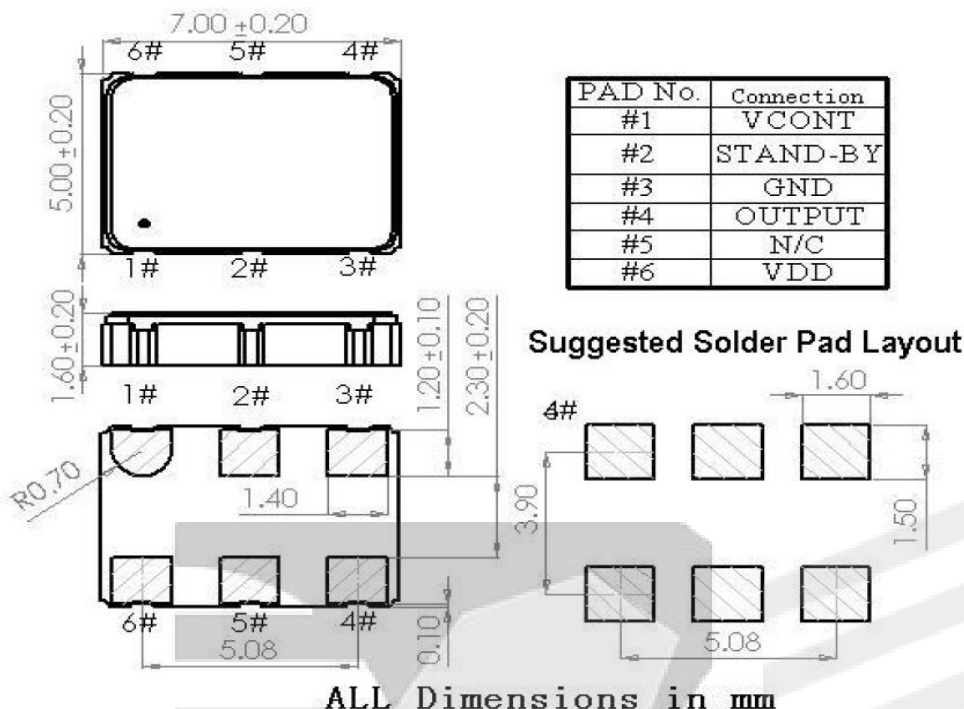


HANGZHOU FREQCONTROL ELECTRONIC TECHNOLOGY LTD.
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Electrical Specification

Parameter	Min		Max		Unit
Supply Voltage ($V_{DD} \pm 10\%$)	$V_{DD}=3.3V$	$V_{DD}=5.0V$	$V_{DD}=3.3V$	$V_{DD}=5.0V$	V
	2.97	4.5	3.63	5.5	
Frequency Range	1		80	50	MHz
Frequency Stability ($V_c = V_{DD}/2$)	Refer to Ordering Guidance (Overall condition Inclusive of calibration @ 25°C, operating temperature change, V_{DD} variation, load variation, aging, etc.)				ppm
Operating Temperature Range	Refer to Ordering Guidance				°C
Absolute Pull Range (APR)	±50, ±75, ±100 typ. Maximum APR of ±150PPM available dependent on Frequency				ppm
Control Voltage Range	0	0.5	3.3	4.5	V
Linearity	-		10		%
Supply Current	-		10	20	mA
	-		15	25	
	-		25	-	
Duty Cycle (CMOS)	45		55		%
Output High (Voh)	0.9 V_{DD}		-		V
Output Low (Vol)	-		0.1 V_{DD}		V
Rise Time/Fall Time (tr/TF)	Measured between 10% to 90% of V_{DD} with output load of 15pF				
	-		6	5	nS
	-		5	4	
	-		3	-	
-		-	-		
Output Load (Fan-out) (CMOS)	15	50	-		pF
Tristate Function	-		-		V
	Output Enable	2	4	(With OE PIN floating, Output enabled)	
Output Disable(Hi-Z)	-		0.5	0.8	V
Start Time	-		10		mS
DC Input Impedance @ Vc pin	2		-		Mohm
Modulation Bandwidth	10		-		KHz
Phase Jitter(RMS, 12KHz to 20MHz)	-		1		pS
Storage Temperature	-55		125		°C

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